

Docket No.: 57454-011



PATENT

#14
Am dt
C
7.2.03
crys

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Akira YAMAZAKI, et al.

Serial No.: 09/780,477 Group Art Unit: 2816

Filed: February 12, 2001 Examiner: Tra, Quan

For: MULTI-POWER SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

RESPONSE

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following claims and remarks are submitted in response to the Office Action dated March 25, 2003. The claims are presented in the revised claim format described in the Pre-OG Notice dated January 31, 2003, regarding waiver of the current provisions of 37 CFR §§ 1.121(a)-1.121(d).

RECEIVED
JUN 26 2003
TECHNOLOGY CENTER 2800